Examiner: Verbitsky, Gail Kaplan

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Inventor(s): Fouad A. Faour et al.
Serial No.: 10/817,265

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2859		
Title: TEMPERATURE MEASUREMENT OF AN INTEGRATED CIRCUIT		
E EXAMINER TO THE ND INTERFERENCES		
sterferences from the decision of the examiner nd 7-19		
pplicable)		
visions of 37 CFR 1.136(a) apply. R 1.136 (fees: 37 CFR 1.17(a)-(d) for the total		
olication.		
However, this conditional petition is being vertently overlooked the need for a petition		
At any time during the pendency of this ayment to Deposit Account 50-3718 pursuant to 37		
Respectfully submitted,		
Fouad A. Faour et al.		
By RW Nekon.		
Robert W. Nelson, Esq. Attorney/Agent for Applicant(s)		
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